

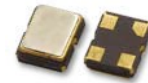
Features

Tri-state operation
Tight Symmetry (45% – 55%) available
Operational Voltage: 1.8, 2.5, 3.3V
Tape & Reel: 1K / 2K / 3K / 5K pcs. per reel
RoHS Compliant Product

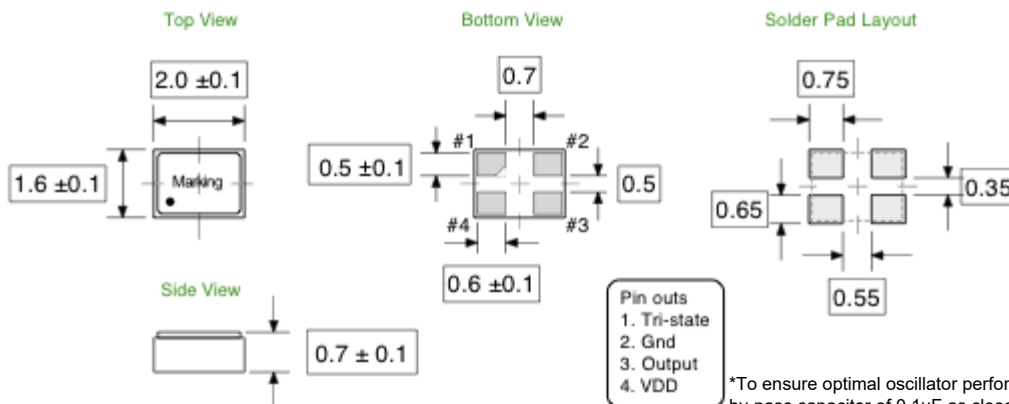
Typical Applications

Mobile Phone, 5G Station
AloT, Meta, ChatGPT,
WiLAN / WiMAX
HDTV

Crystal Oscillator
2.0 x 1.6 x 0.8mm Ceramic SMD
1.0MHz ~ 80.0MHz



Outlines & Dimensions (mm)



*To ensure optimal oscillator performance, place a by-pass capacitor of 0.1uF as close to the part as possible between VDD and GND pads.

Electrical Specifications

Parameter	Specifications			Unit
Frequency Range	1.0 ~ 80.0			MHz
Frequency Tolerance (@25°C)	±10, ±20, ±30, ±50, ±100			ppm
Supply Voltage	3.3 ±0.33, 2.5 ±0.25, 1.8 ±0.18			V _{DD}
Current Consumption *	5 (typ.) / 20MHz@3.3V			mA
	8 (typ.) / 50MHz @ 3.3V			
Output	CMOS @ 1.8 ~ 3.3, TTL @ 3.3			V _{DD}
Symmetry	40/60, 45/55			%
Tri-State (Input to Pin 1)				
Output High (Logic “1”)	Output Enabled			
Output Low (Logic “0”)	Output Disabled (High Impedance Output)			
Storage Temp. Range	-40 ~ +85			°C
Operating Temp. Range	-10 ~ +60	-20 ~ +70	-40 ~ +85	
Frequency vs. Temp. (@25°C)	±15,	±20	±30	ppm
	±30	±30	±50	
	±50	±50	±100	
	±100	±100	-	

* Frequency & Supply Voltage dependent

Bypass capacitor should be connected between V_{DD} & Gnd for stable operation.



Specifications subject to change without notice.



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